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Docket No.: P2001,0097

OCT 06 2006

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MAIL STOP: APPEAL BRIEF-PATENTS

By: D. Richter

Date: October 6, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Before the Board of Patent Appeals and Interferences

Applic. No. : 10/075,656 Conf. No.: 9370
Inventor : Detlev Richter
Filed : February 13, 2002
Title : Semiconductor Module with a Configuration for the Self-Test of a Plurality of Interface Circuits and Test Method
TC/A.U. : 2133
Examiner : John J. Tabone, Jr.
Customer No. : 24131

Hon. Commissioner for Patents
Alexandria, VA 22313-1450

RESPONSE TO NOTIFICATION OF NON-COMPLIANT APPEAL BRIEF

Mail Stop Appeal Brief-Patents
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Notification of Non-Compliant Appeal Brief, dated November 6, 2006, kindly consider the following:

Remarks/Arguments begin on page 2 of this paper.